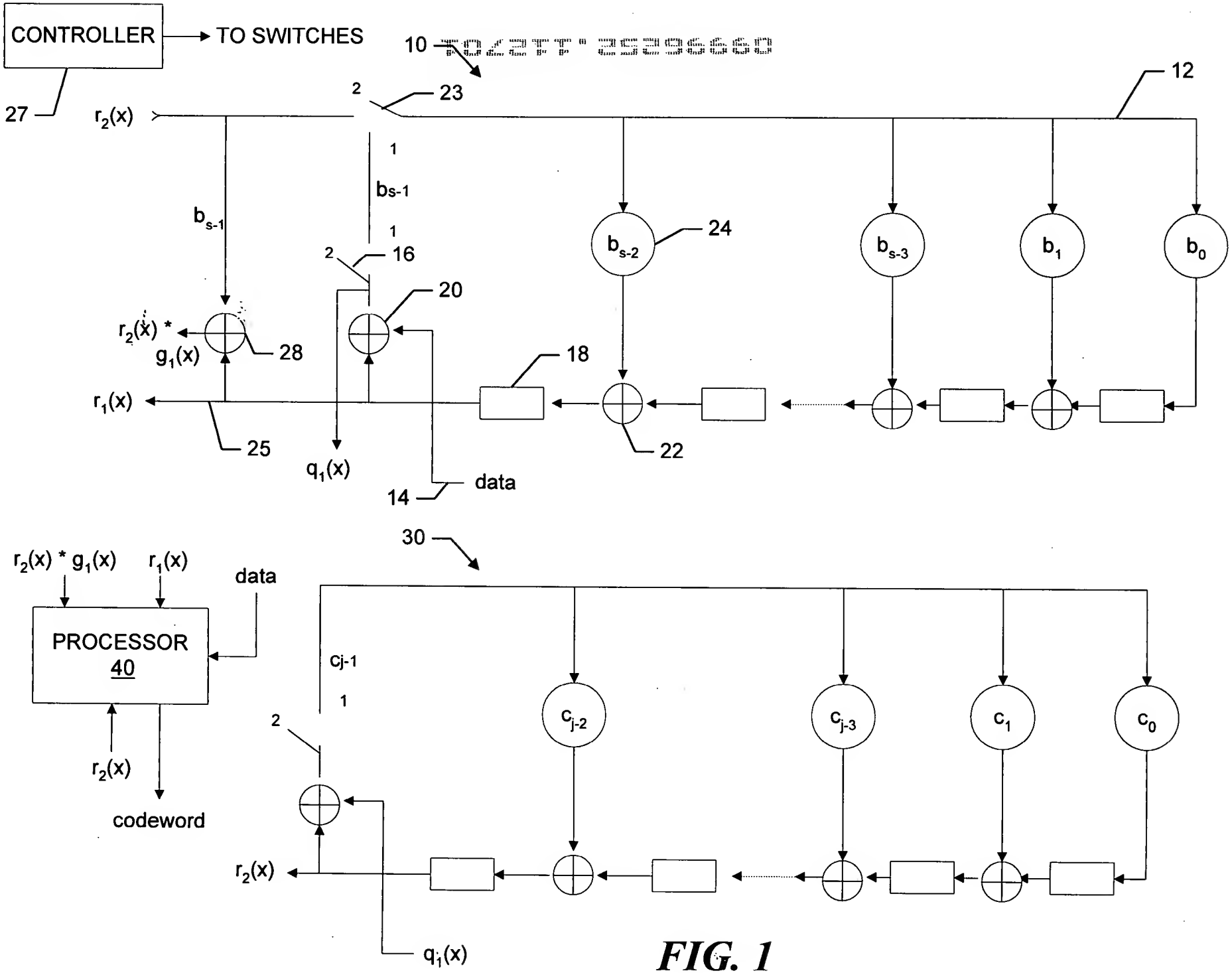
CONTROLLER
TO SWITCHES
10
27
12
23
$r_2(x)$
b_{s-1}
b_{s-2}
b_{s-3}
b_1
b_0
24
16
20
$r_2(x) * g_1(x)$
28
18
$r_1(x)$
25
22
$q_1(x)$
data
14
30
$r_2(x) * g_1(x)$
$r_1(x)$
data
PROCESSOR
40
$r_2(x)$
codeword
c_{j-1}
c_{j-2}
c_{j-3}
c_1
c_0
$r_2(x)$
$q_1(x)$

FIG. 1

Patent Application

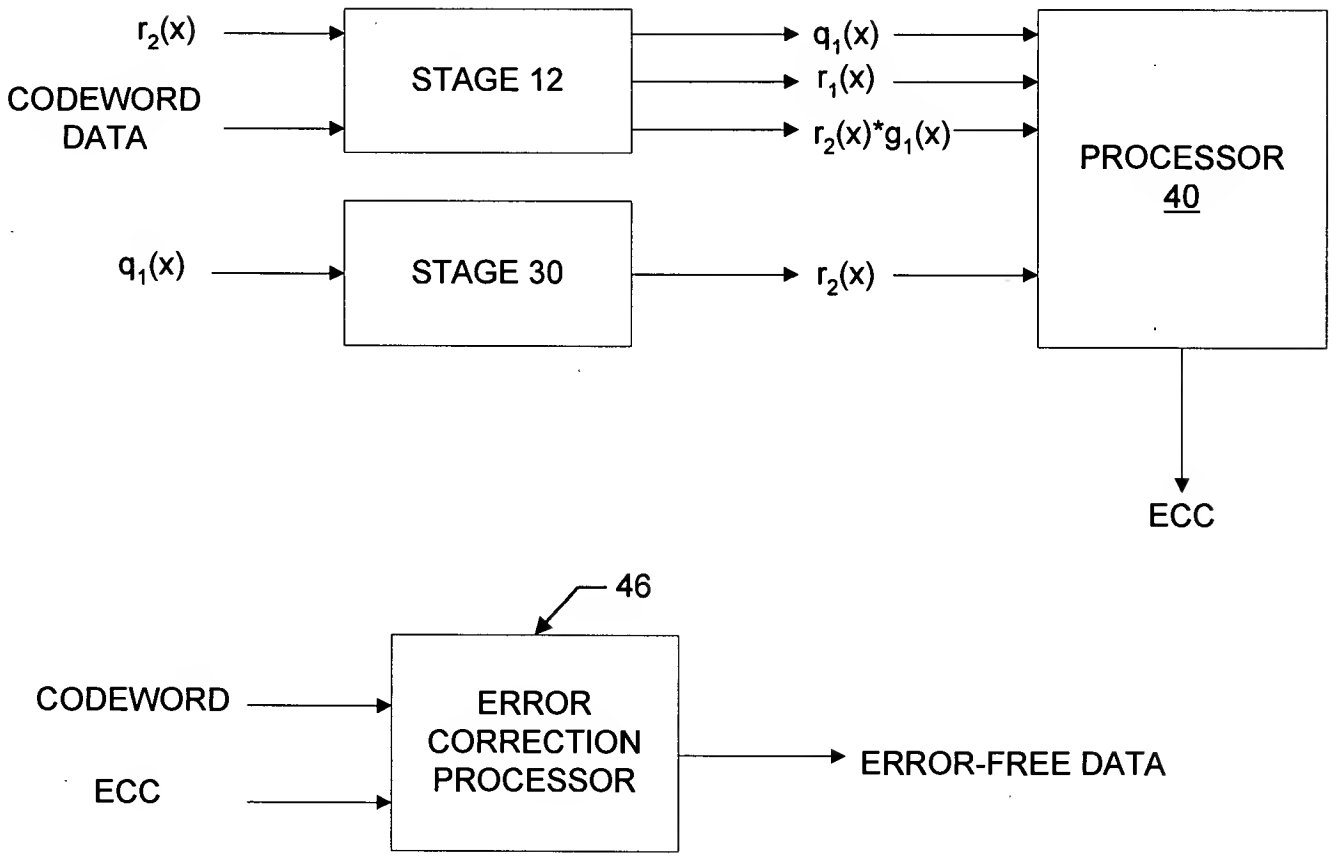

FIG. 2

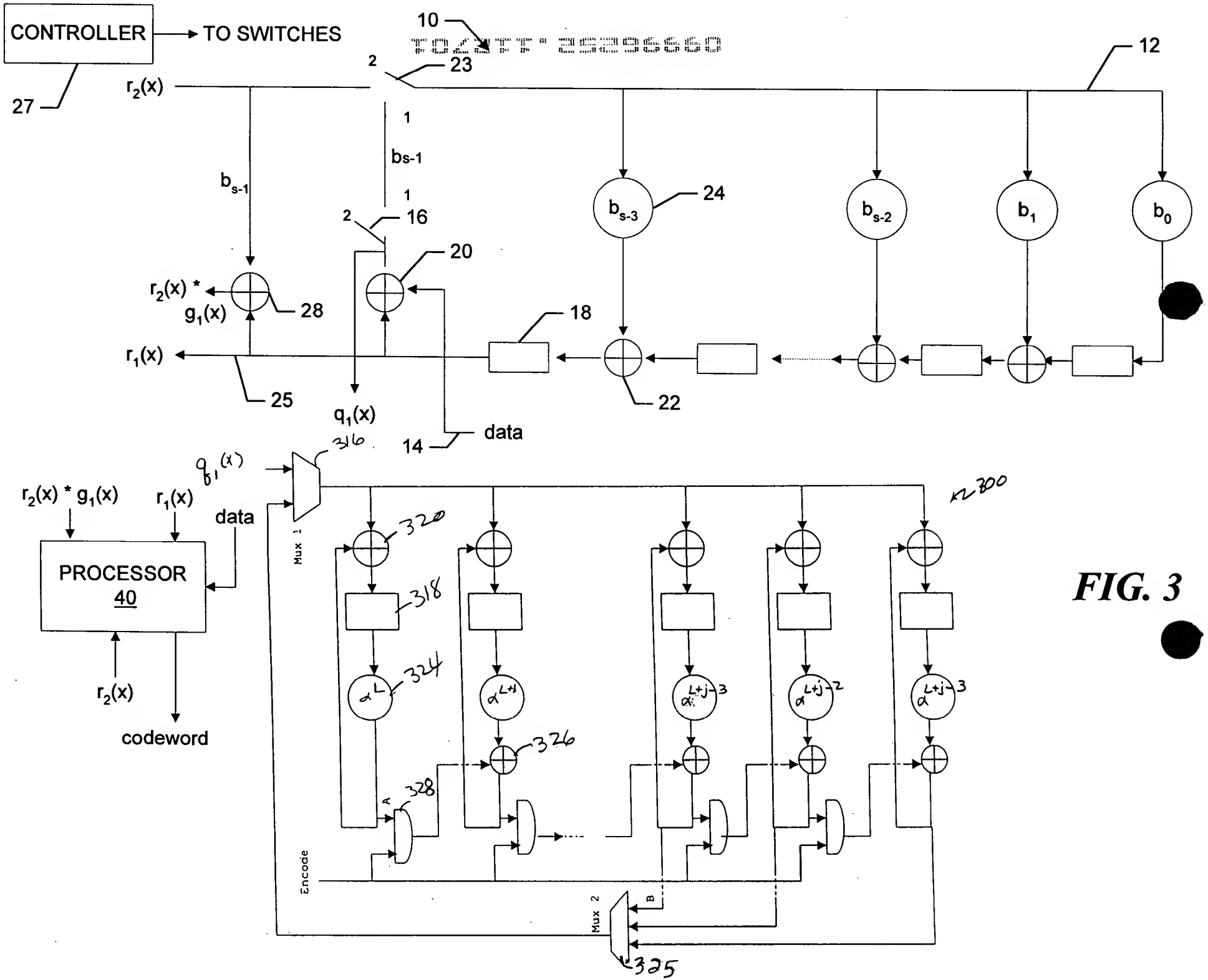
CONTROLLER
TO SWITCHES
10
12
23
27
$r_2(x)$
b_{s-1}
bs-1
b_{s-3}
24
b_{s-2}
b_1
b_0
16
20
$r_2(x) * g_1(x)$
28
18
$r_1(x)$
25
22
$q_1(x)$
data
14
316
$q_{b1}(x)$
$r_2(x) * g_1(x)$
$r_1(x)$
data
PROCESSOR
40
$r_2(x)$
codeword
Mux 1
320
318
324
326
328
Encode
Mux 2
325
300

FIG. 3

TOZATT 2636660

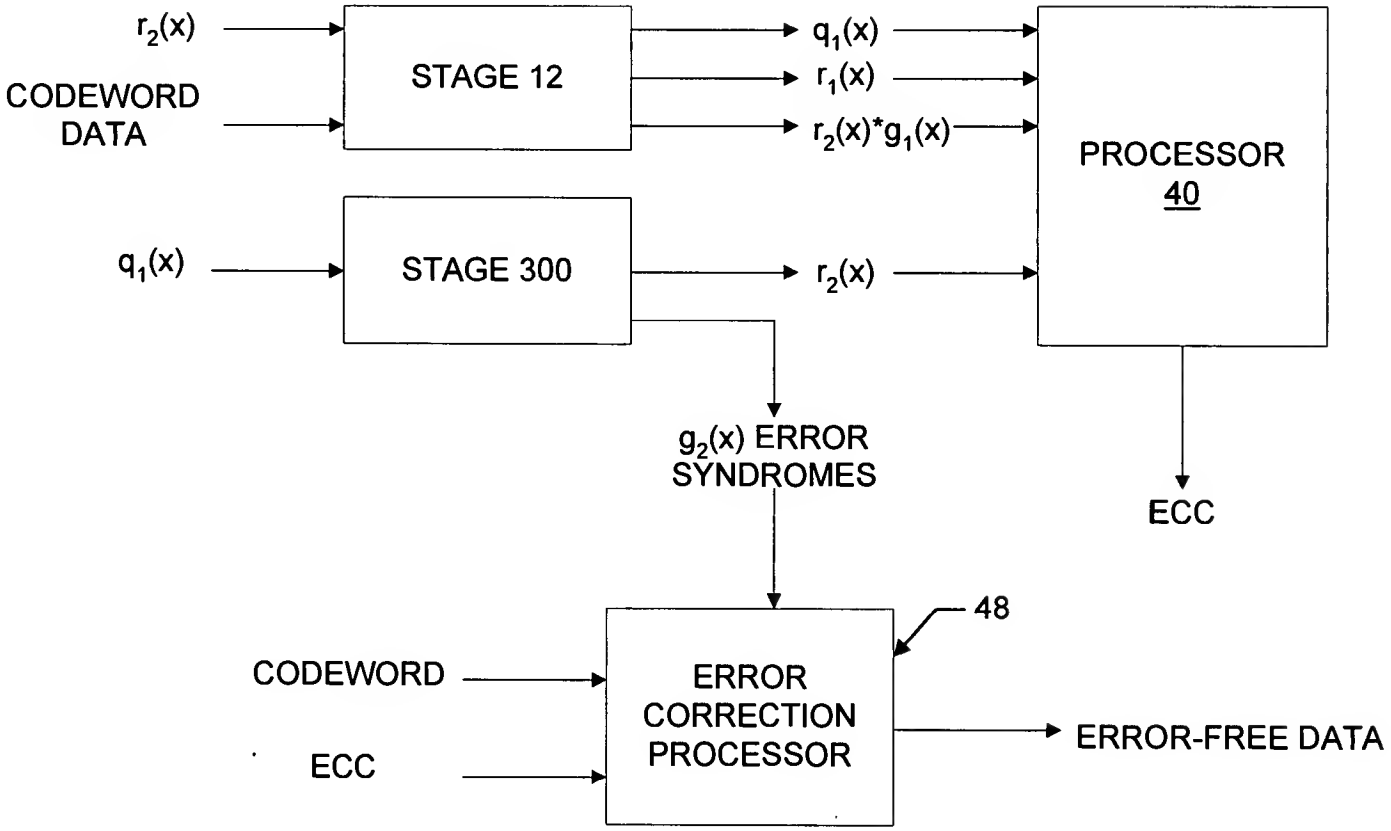

FIG. 4